

A REQUEST TRACKING DATA PREFETCHER APPARATUS

ABSTRACT OF THE DISCLOSURE

A request tracking data prefetcher apparatus for a computer system. The apparatus
5 includes a prefetcher coupled to a memory of the computer system. A tracker is coupled to
the prefetcher, and is configured to recognize an access to a plurality of cache lines of the
memory by a processor of the computer system. A cache memory is coupled to the
prefetcher. The prefetcher predictively loads a target cache line of the memory into the cache
memory to reduce an access latency of the processor. The target cache line for the predictive
10 load is indicated by the tracker.